

# MATTINGLY, STANGER & MALUR, P.C.

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PATENT, TRADEMARK  
AND COPYRIGHT LAW

FACSIMILE: (703) 684-1157

15915 U.S. PTO  
09/10/03

(703) 684-1120

Date: September 10, 2003

CUSTOMER NO. 24956

Attorney Docket No. H-1111

To: Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Sir: Transmitted herewith for filing is the patent application of:

Inventor: S. KONISHI et al (see attached)

For:  
A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Enclosed are:



25 Sheets of Drawings



This application is being filed without an executed Declaration.



Priority is claimed from Japanese Application No. 2002-281607 filed September 26, 2002.  A certified copy is attached herewith. (which is hereby



Copies of the disclosure documents listed on the attached PTO 1449 form and discussed in the specification or  attached Information Disclosure Statement.

incorporated by reference)



Small entity status under 37 CFR 1.9 and 1.27 is hereby claimed.



Specification: Abstract  , Description 33 pages; and 16 claim(s).



Preliminary Amendment.



Executed Declaration.

The filing fee is calculated as shown below:

Small Entity

Large Entity

For:	No. Filed	No. Extra
Basic Fee		
Total Claims	16 -20 = * 0	
Indep Claims	2 - 3 = * 0	
<input type="checkbox"/> Multiple Dependent Claim (s)		

\* If difference is less than zero  
then enter '0' in second column

Rate	Fee
	\$ 375
x 9	\$
x 42	\$
+ 140	\$
Total	\$

Rate	Fee
	\$ 750
x 18	\$ 0
x 84	\$ 0
+ 280	\$ 0
Total	\$ 750



A check or credit card payment form in the amount of \$ 750.00 is enclosed for the filing fee.



The Commissioner is hereby authorized to charge any additional fees that may be required to Deposit Account No. 50-1417.

Respectfully Submitted,

By:

John R. Mattingly

Registration No. 30,793

19704 U.S. PTO  
10/658402  
09/10/03